

coordinates and a design exchange cell based on the error position and cell based on the error coordinates determine error position within the 26 determine an affected input of the 28 interpret an integrated circuit error report to obtain error coordinates determine a cell of the integrated 30 charge protection element to the logically couple the available identify an available charge circuit based on the error affected input of the cell a library exchange file protection element format file start

Figure 2

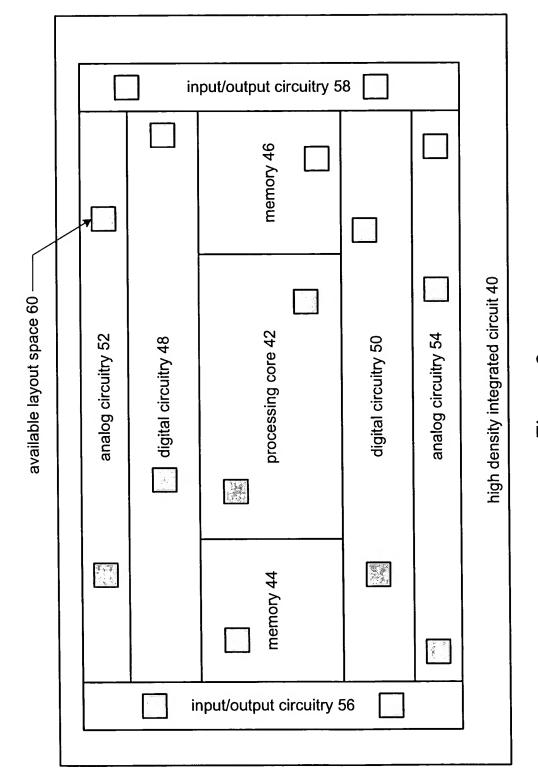


Figure 3 initial IC layout

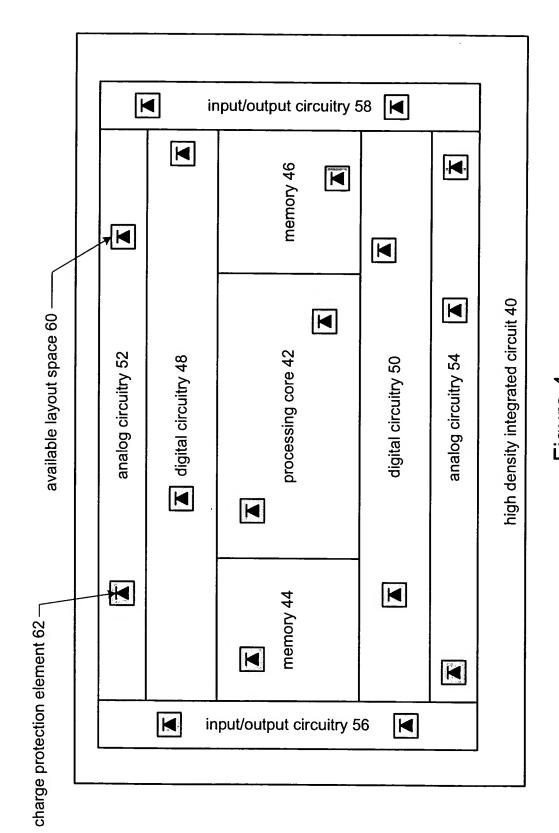


Figure 4 intervening IC layout

